

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-65. (Canceled)

66. (New) An active matrix substrate, comprising:

a scan line;

a data line;

a pixel transistor corresponding to an intersection of the scan line and the data line;

a first data line driving circuit connected to a first end of the data line;

a second data line driving circuit connected to a second end of the data line;

an analog video signal line providing an analog video signal to the first data driving circuit; and

a digital video signal line providing a digital video signal to the second data driving circuit,

the first driving circuit including a first shift resistor, a first gate circuit that inputs an output signal from the first shift resistor, a switch circuit that outputs the analog video signal to the first end of the data line by an output signal from the gate circuit,

the second driving circuit including a second shift resistor, a second gate circuit that inputs an output signal from the second shift resistor, a latch circuit that outputs the digital video signal, the latch circuit being connected to the second gate circuit and the digital video signal line, a D/A converter that converts the digital video signal into an analog signal, the analog signal being output to the second end of the data line.

67. (New) The active matrix substrate according to claim 66, the latch circuit including a first latch circuit and a second latch circuit, the first latch circuit being connected

to the second gate circuit and the digital video signal line, the second latch circuit outputting the digital video signal to the D/A converter.

68. (New) An active matrix substrate, comprising:

- a plurality of scan lines;
- a plurality of data lines;
- a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;
- a first data line driving circuit connected to a first end of each of the plurality of data lines;
- a second data line driving circuit connected to a second end of each of the plurality of data lines;
- at least one analog video signal line providing an analog video signal to the first data driving circuit; and
- at least one digital video signal line providing a digital video signal to the second data driving circuit.

69. (New) The active matrix substrate according to claim 68, the first data line driving circuit including at least one shift resistor, at least one gate circuit, and a plurality of switch circuits.

70. (New) The active matrix substrate according to claim 69, the plurality of switch circuits including a first group and a second group, the first group being controlled by first output signals from the at least one gate circuit simultaneously, the second group being controlled by second output signals from the at least one gate circuit simultaneously.

71. (New) The active matrix substrate according to claim 70, one of the switches of the first group being located between two of the switches of the second group.

72. (New) The active matrix substrate according to claim 68, the second data line driving circuit including a D/A converter, the D/A converter converting the digital video signal into an analog signal, the second data line driving circuit outputting the analog signal to the second end of at least one of the second data lines.

73. (New) The active matrix substrate according to claim 68, the second data line driving circuit being connected to the at least one digital video signal line, the second data line driving circuit including a first latch circuit that hold the digital video signal temporarily.

74. (New) The active matrix substrate according to claim 73, the second data line driving circuit including a second latch circuit that is connected to the first latch circuit, the second latch circuit holding a data of each bit of the first latch circuit simultaneously.

75. (New) The active matrix substrate according to claim 68, the second data line driving circuit being a point sequential digital driver.

76. (New) The active matrix substrate according to claim 68, the second data line driving circuit being a line sequential digital driver.

77. (New) The active matrix substrate according to claim 68, the first data line driving circuit including a shift resistor and a gate circuit, the gate circuit including at least a NAND gate, the NAND gate inputting an output signal from the shift resistor and an enable signal.

78. (New) The active matrix substrate according to claim 68, the first data line driving circuit including a shift resistor and a gate circuit, the gate circuit including at least a XOR gate, the XOR gate inputting at least two output signals from the shift resistor.

79. (New) An active matrix substrate, comprising:

a plurality of scan lines;

a plurality of data lines;

a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;

a first data line driving circuit connected to a first end of each of the plurality of data lines; and

at least one analog video signal line providing an analog video signal to the first data driving circuit,

the first data line driving circuit including a shift resistor and a gate circuit, the gate circuit including at least a NAND gate, the NAND gate inputting an output signal from the shift resistor and an enable signal.

80. (New) An active matrix substrate, comprising:

a plurality of scan lines;

a plurality of data lines;

a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;

a first data line driving circuit connected to a first end of each of the plurality of data lines; and

at least one analog video signal line providing an analog video signal to the first data line driving circuit,

the first data line driving circuit including a shift resistor and a gate circuit, the gate circuit including at least a XOR gate, the XOR gate inputting at least two output signals from the shift resistor.

81. (New) The active matrix substrate according to claim 68, the second data line driving circuit including a plurality of driving transistors, the plurality of driving transistors being formed on a substrate, the plurality of pixel transistors being formed on the substrate.

82. (New) The active matrix substrate according to claim 81, the plurality of driving transistors and the plurality of pixel transistors being formed on a same layer of the substrate.

83. (New) A display device comprising the active matrix substrate according to claim 68.